

J1060 U.S. PTO  
10/017389  
12/18/01

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10017389	12 18 2001	257		2811	

**\*\*APPLICANTS:** Ishibashi Akihiko, Tsujimura Ayumu, Kawaguchi Yasutoshi, Otsuka Nobuyuki, Ohnaka Kiyoshi

**\*\*CONTINUING DATA VERIFIED:** *yes* *OK*

**\*\* FOREIGN APPLICATIONS VERIFIED:** *yes* *OK*  
JAPAN 2000-386624 12/20/2000

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 condition met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledge Examiners' signals		ATTORNEY DOCKET NO 740819-720
<b>TITLE :</b> Method for fabricating semiconductor, method for fabricating semiconductor substrate, and semiconductor light emitting device		

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Primary Examiner PREPARED FOR ISSUE Application Examiner	
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